

An Efficient Simulation Algorithm for Cache of Random Replacement Policy

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Zheng Zhou, Sep. 13, 2010

Organization

- Background
- Algorithm
- Evaluation

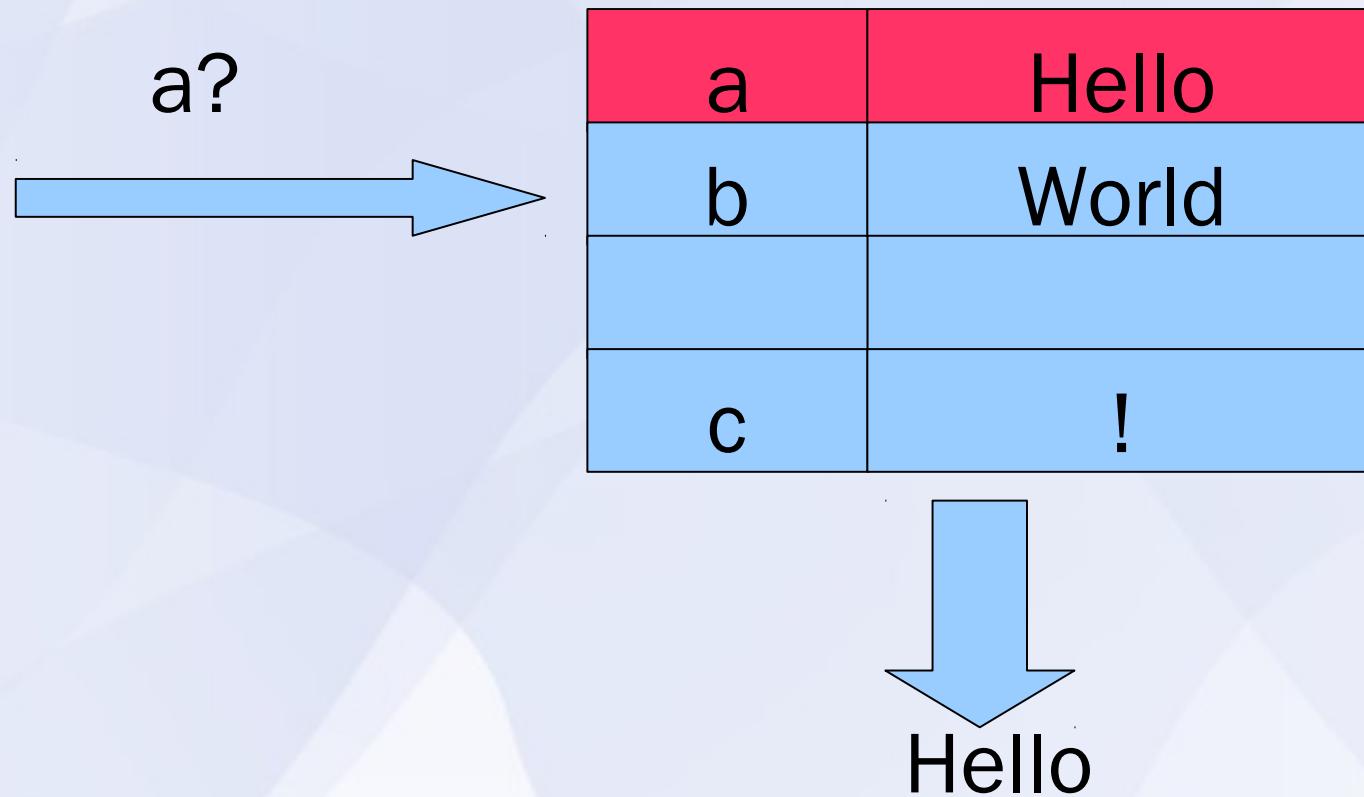
Cache Hit

a?



a	Hello
b	World
c	!

Cache Hit



Cache Miss

d?
→

a	Hello
b	World
c	!

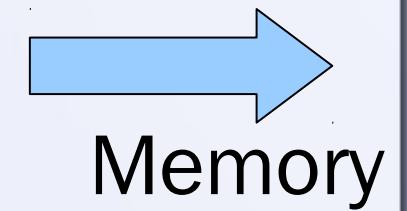
Cache Miss

d?



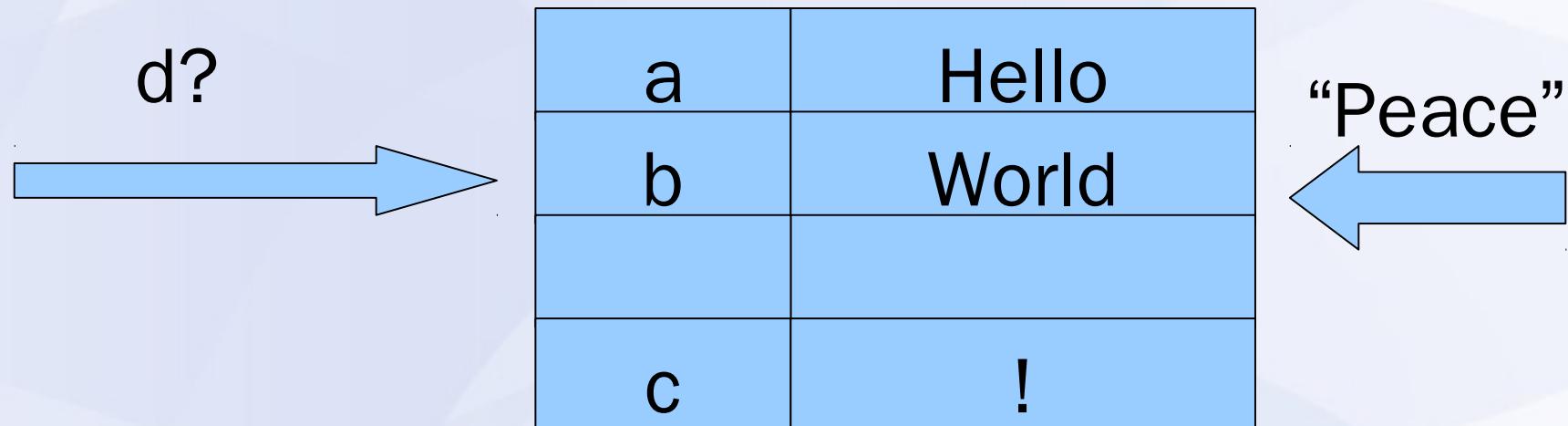
a	Hello
b	World
c	!

Next
Level or



Memory

Cache Miss



Where to store the new pair?

Evict the oldest

- * Least Recently Used

- ** found in Intel/AMD

Randomly pick a slot

- * Random Replacement

- ** found in ARM/Loongson

Random Replacement

d	Peace
b	World
c	!

Random Replacement

a	Hello
d	Peace
c	!

Random Replacement

a	Hello
b	World
d	Peace
c	!

Random Replacement

a	Hello
b	World
d	Peace

Different Behavior

a?

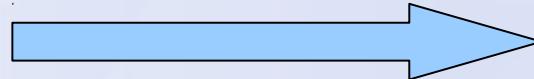


d	Peace
b	World
c	!

A blue thought bubble contains the word 'Miss'.

Different Behavior

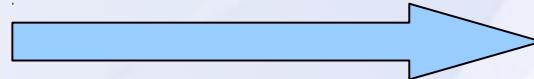
a?



d	Peace
b	World
c	!

A light blue cloud-shaped bubble containing the word "Miss".

a?

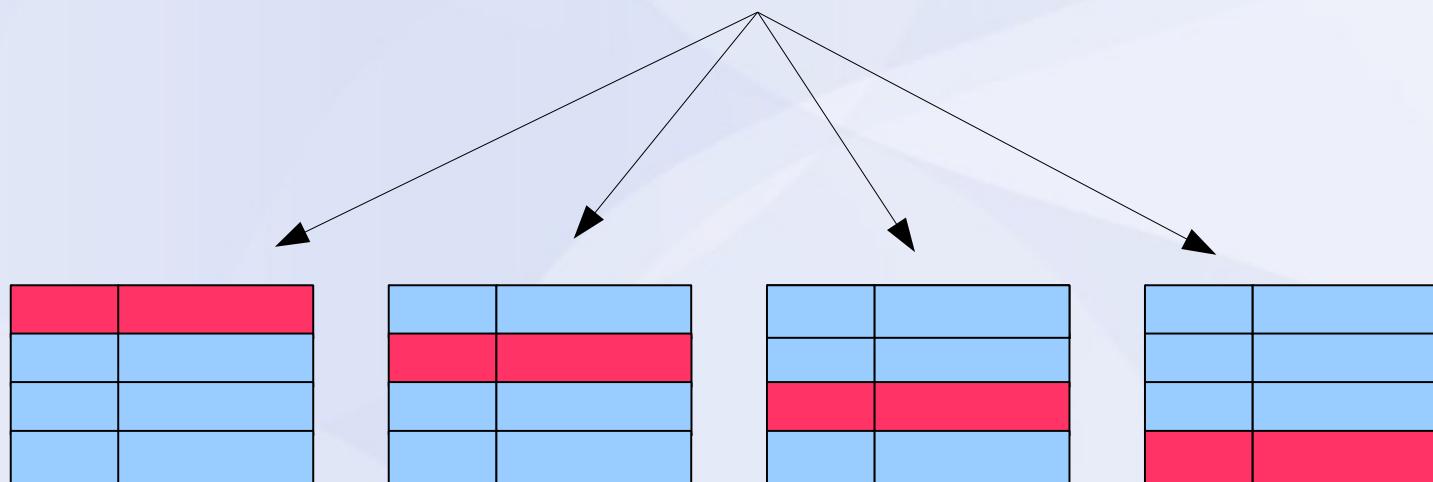


a	Hello
b	World
d	Peace

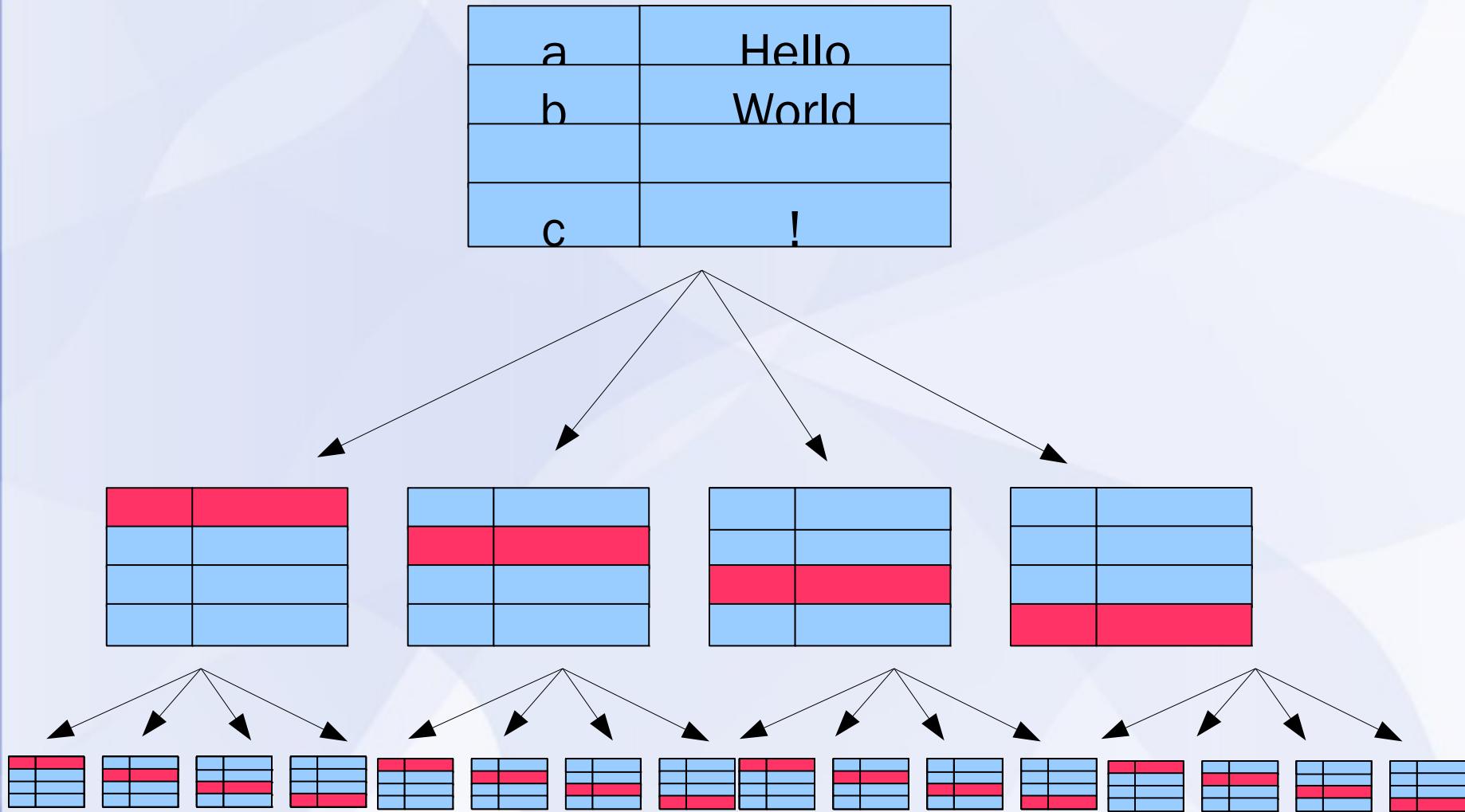
A light blue cloud-shaped bubble containing the word "Hit".

Combinatorial Explosion

a	Hello
b	World
c	!



Combinatorial Explosion



Solution?

Solution?

Recast the problem in probability setting.

Settings

- * Let the input be the sequence of cache line index
 - ** $a_0, a_1, a_2, \dots a_n$,
- * Assume associativity to be M
 - ** there are M candidates for eviction upon a cache miss

Indicator Random Variable

$X=1$ if an event happens

$X=0$ if an event *does not* happen

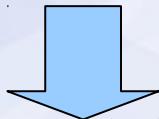
$$E(X) = 1 \cdot P(X=1) + 0 \cdot P(X=0) = P(X=1)$$

The expectation equals the probability of an event.

Indicator Random Variable

Let X_i indicate the miss event for a_i

$a_0, a_1, a_2, \dots a_n$



$X_0, X_1, X_2, \dots X_n$

Reuse window



The diagram illustrates the concept of window reuse. It features a sequence of letters: 'a', 'b', 'a', 'c', 'd', 'b'. Above the first 'a' is a blue curved arrow pointing downwards. Below the sequence is a larger, semi-transparent blue curved arrow pointing upwards, spanning from the end of the sequence back towards the start.

a,b,a,c,d,b

Reuse window



$Z_i = \infty$ if a_i never occurs before

$Z_i = \text{sum of } X_i \text{ in the reuse window}$

Key Observations

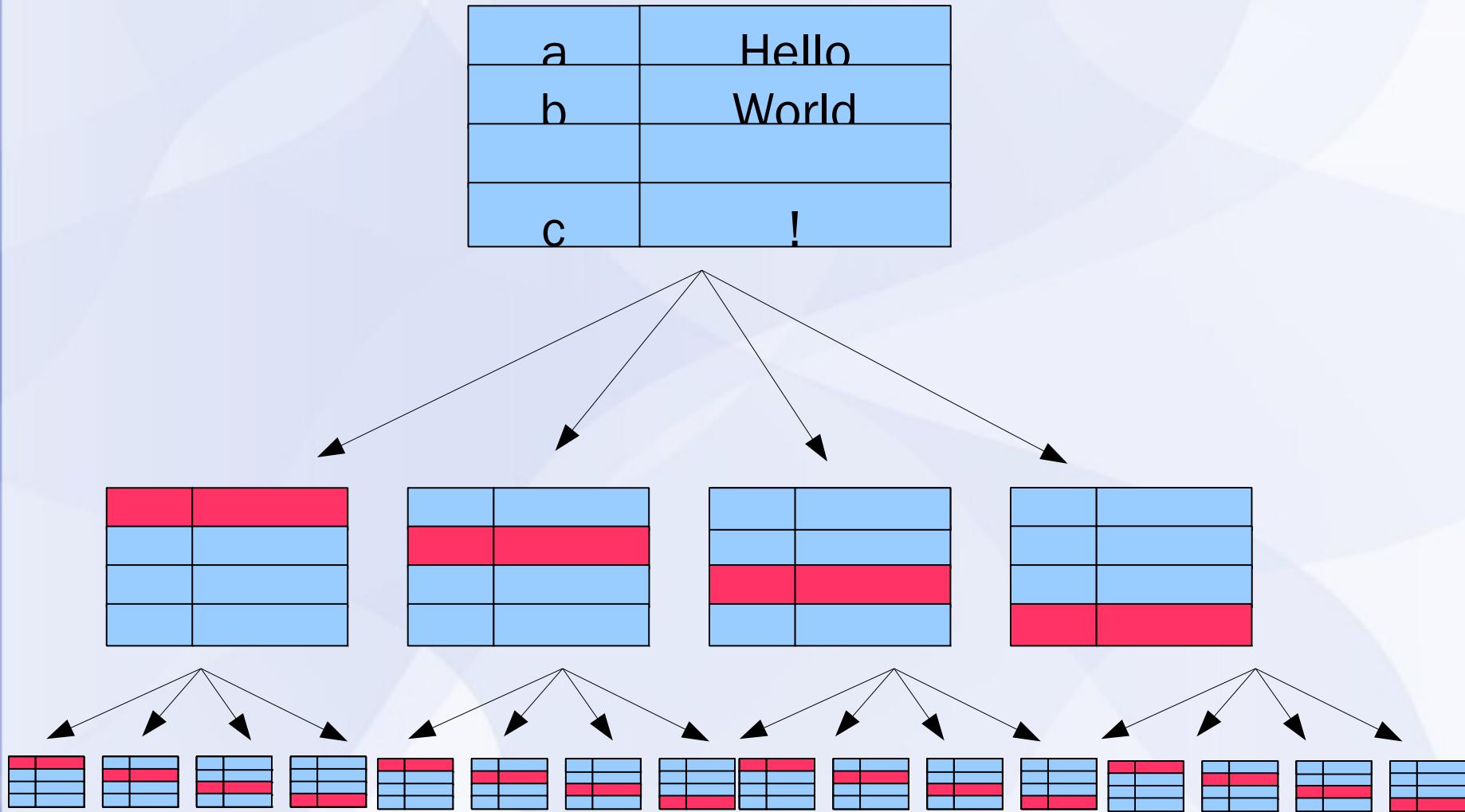
A cache line is definitely in cache *after the access*

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Every cache miss will have a $1/M$ probability of evicting a cache line in the same cache set.

Combinatorial Explosion



Key Observations

A cache line is definitely in cache *after the access*

Every cache miss will have a $1/M$ probability of evicting a cache line in the same cache set.

A cache line will be in the cache with $(1-1/M)^Z$ probability, if there were Z misses in the reuse window.

Foundation

$$E(X_i|Z_i) = 1 - (1 - 1/M)^{Z_i}$$

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$$E(X_i) = 1 - E((1 - 1/M)^{Z_i})$$

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$$E(X_i|Z_i) = 1 - (1 - 1/M)^{Z_i}$$

$$E(X_i) = 1 - E((1 - 1/M)^{Z_i})$$

Impossible to solve directly, for the correlation
between consecutive miss events.

Approximation

$$\begin{aligned} E(X_i) &= 1 - E((1 - 1/M)^{Z_i}) \\ &\approx 1 - (1 - 1/M)^{E(Z_i)} \end{aligned}$$

See paper for precision of approximation

Reuse window



$Z_i = \infty$ if a_i never occurs before

$Z_i = \text{sum of } X_i \text{ in the reuse window}$

Reuse window



a,b,a,c,d,b

$EZ_i = \infty$ if a_i never occurs before

$EZ_i = \text{sum of } EX_i \text{ in the reuse window}$

Approximation

We just formulate a circular relation between
 EX_i and EZ_i

Can solve and get all EX_i , hence knowing the
hit/miss probability of each cache
reference.

EZ, EX, M=4

a, b, a, c, d, b

EZ

EX

EZ, EX, M=4

a, b, a, c, d, b

EZ ∞

EX

EZ, EX, M=4

a, b, a, c, d, b

EZ ∞

EX 1

EZ, EX, M=4

a, b, a, c, d, b

EZ ∞ ∞

EX 1 1

EZ, EX, M=4

a, b, a, c, d, b

EZ ∞ 1

EX 1 1

EZ, EX, M=4

a, b, a, c, d, b

EZ $\propto \infty$ 1

EX 1 1 $\frac{1}{4}$

EZ, EX, M=4

a, b, a, c, d, b

EZ ∞ ∞ 1 ∞ ∞ 2.25

EX 1 1 $\frac{1}{4}$ 1 1 0.48

EZ, EX, M=4

a, b, a, c, d, b

EZ ∞ ∞ 1 ∞ ∞ 2.25

EX 1 $1 \frac{1}{4}$ 1 1 0.48

#Total misses = sum of EX

≈ 4.73

Efficient Implementation

Problem: Upon each cache miss, all EZ_i need be updated.

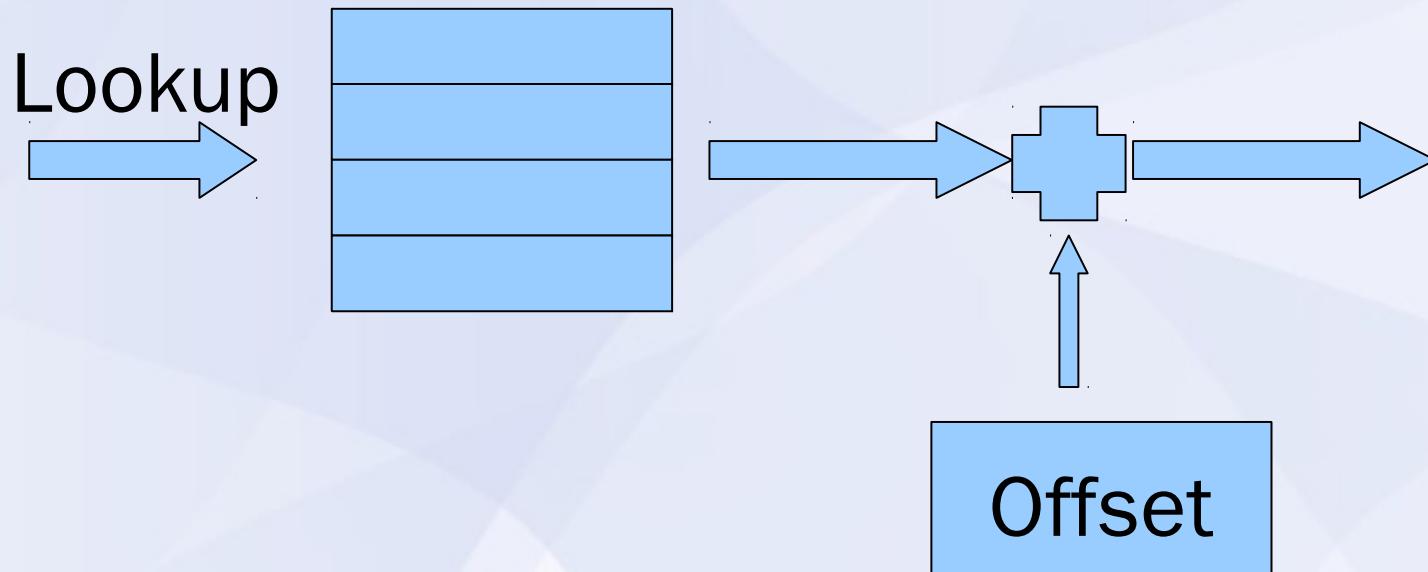
Efficient Implementation

Problem: Upon each cache miss, all EZ_i need be updated.

- * But almost all incremented by the same value: EX_i , except the one corresponding to a_i is set to 0

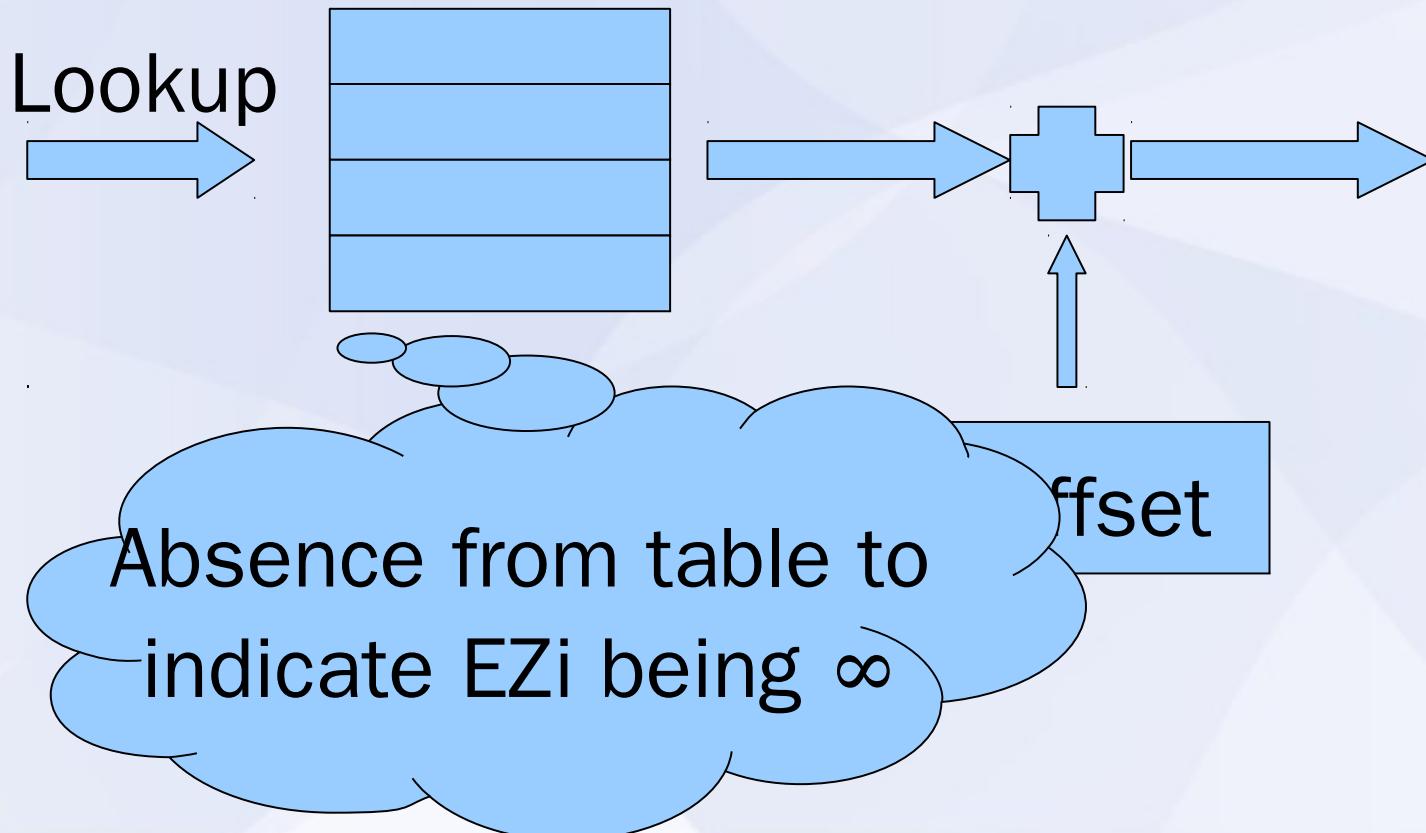
Efficient Implementation

Use a hash map with an offset to store EZ_i



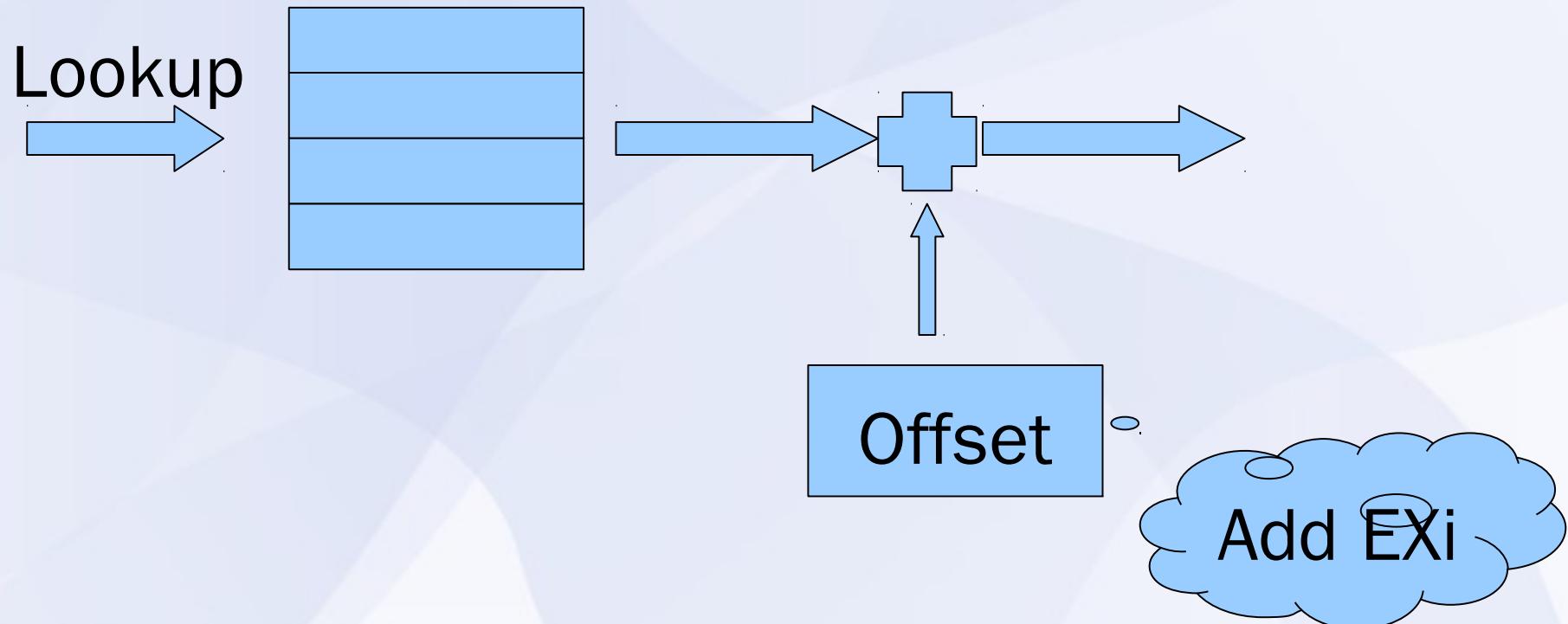
Efficient Implementation

Use a hash map with an offset to store EZ



Efficient Implementation

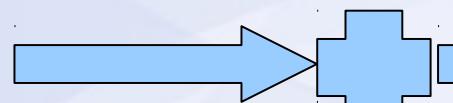
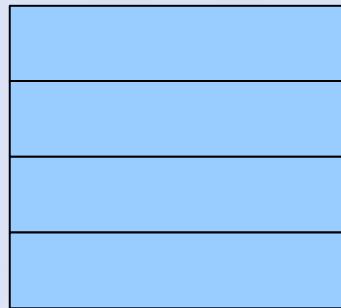
Use a hash map with an offset



Efficient Implementation

Use a hash map with an offset

Lookup



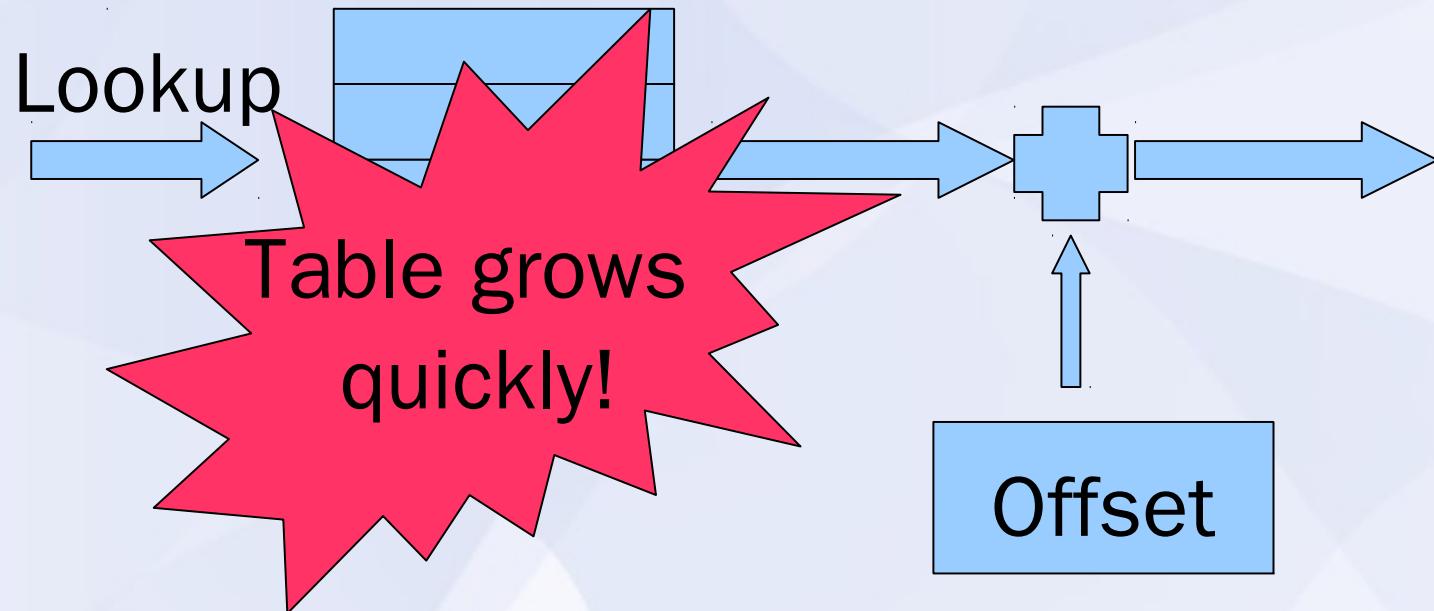
Offset

Linear time

Add E_i

Control Space

Use a hash map with an offset



Another Approximation

Tolerate a little absolute error of the hit probability $1 - E(X_i)$

Another Approximation

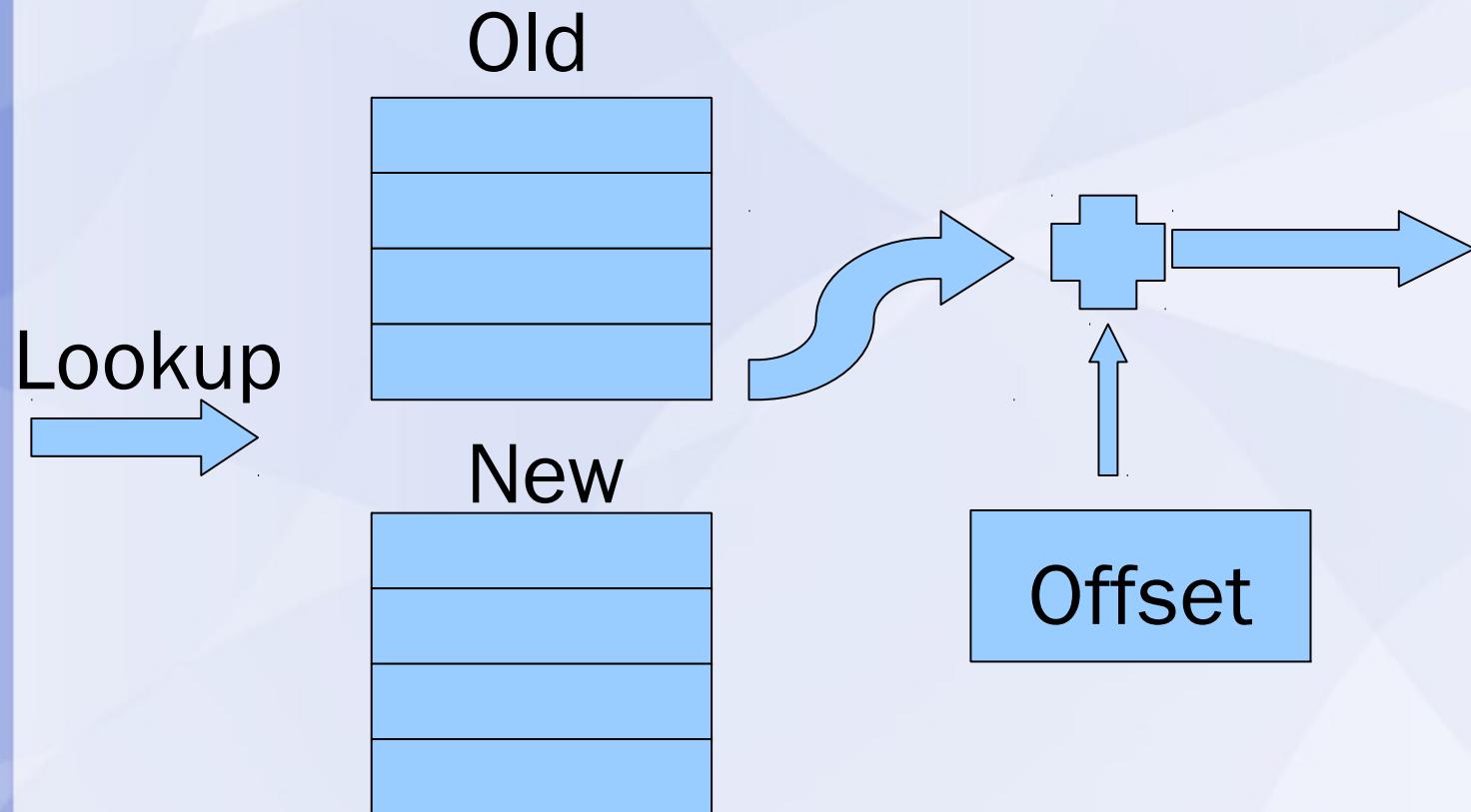
Tolerate a little absolute error of the hit probability $1 - E(X_i)$

Then can set large EZ_i to ∞

* The same as removing from the table

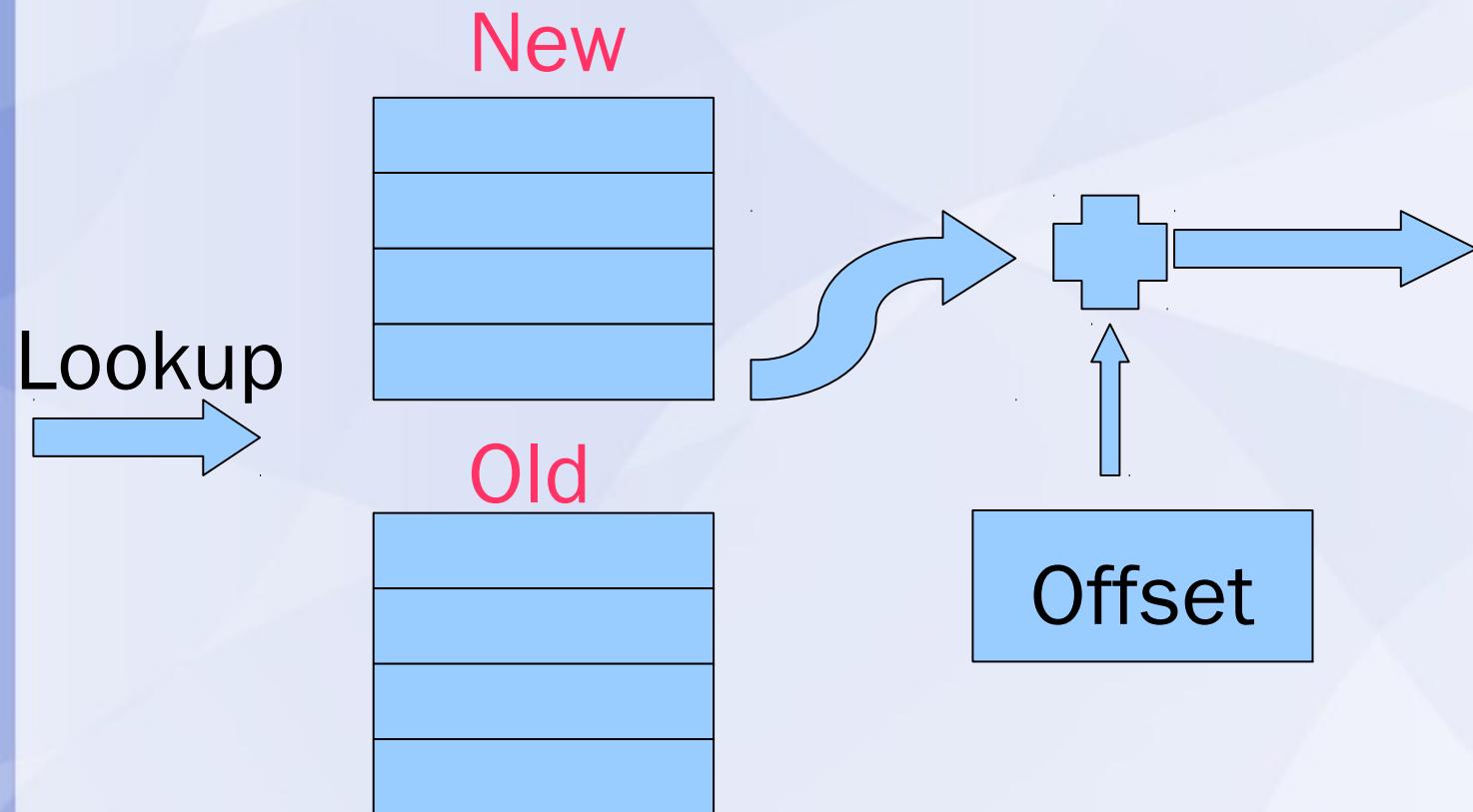
Sliding Windows

Use two hash maps with an offset to store EZ_i



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Use two hash maps with an offset to store EZ_i



Summary

Time: linear

Space: $\log \varepsilon / \log (1-1/M) \approx M \log(1/\varepsilon)$

Precision: smaller with larger M

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Space: $\log \varepsilon / \log (1-1/M) \approx M \log(1/\varepsilon)$

Precision: smaller with larger M

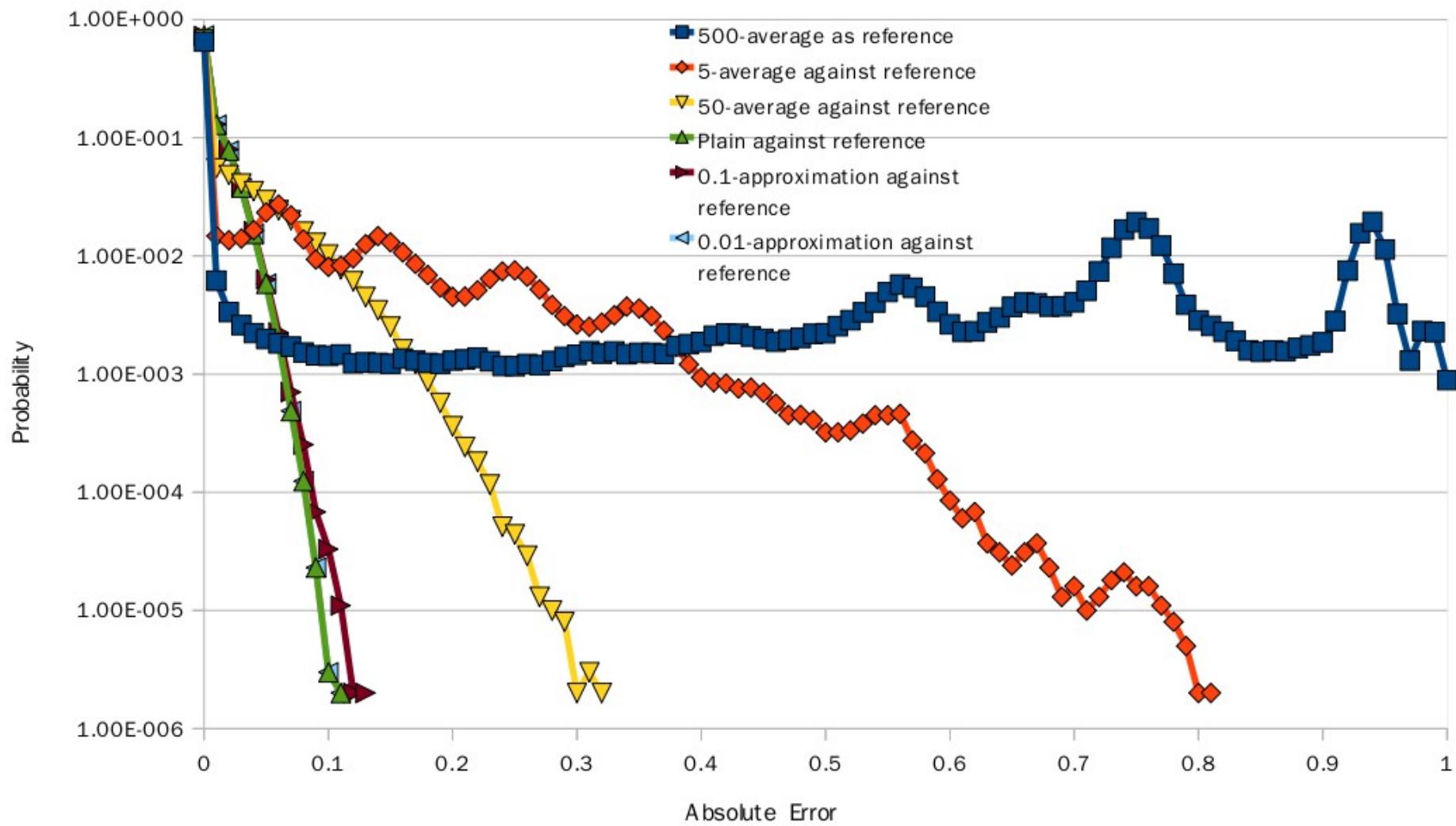
Extends to set-associative cache

Evaluation Setup

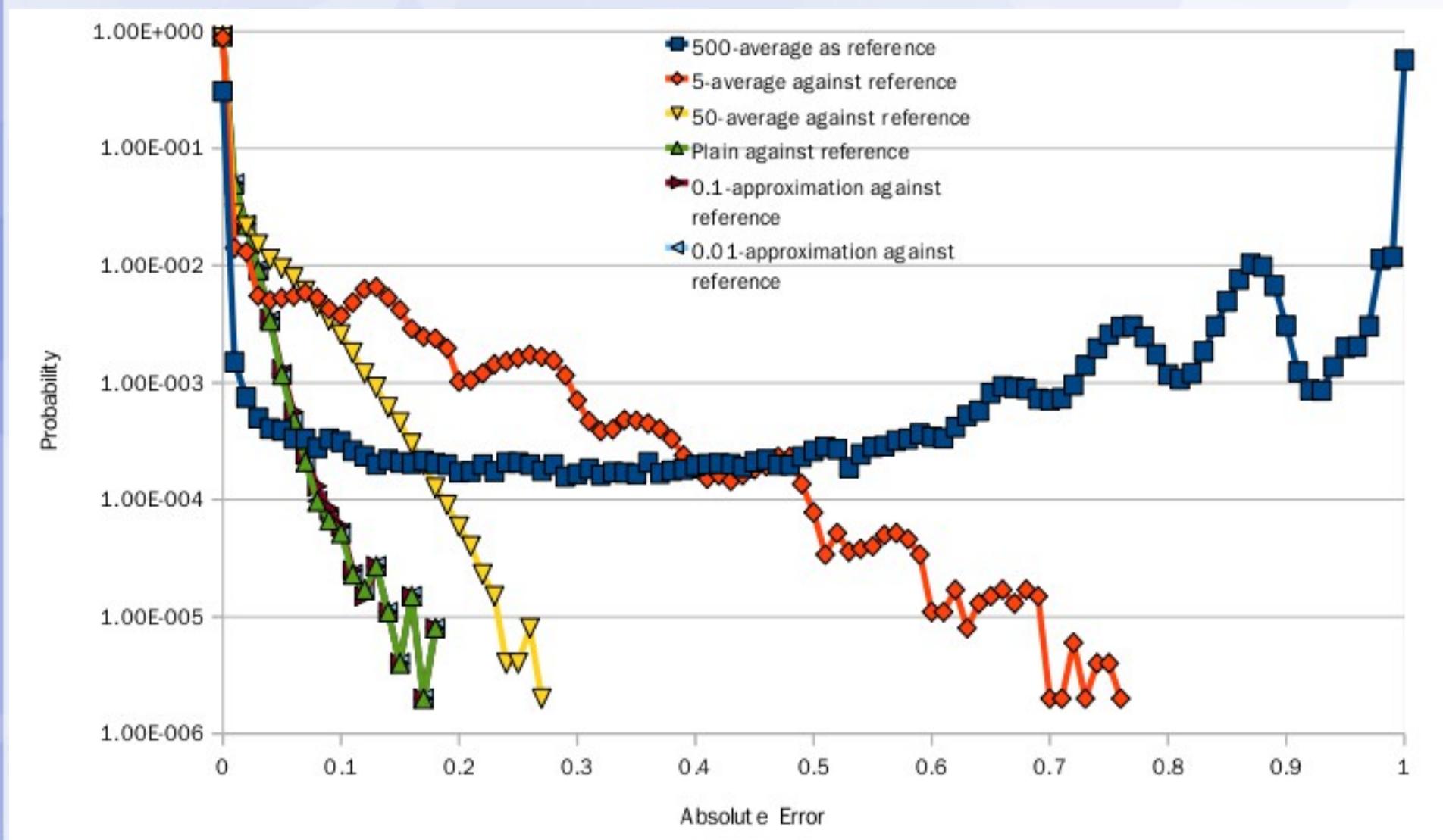
Evaluate against multiple rounds of Monte Carlo simulation

Realistic traces (1GB) collected by HMTT for CPU2000/LINPACK.

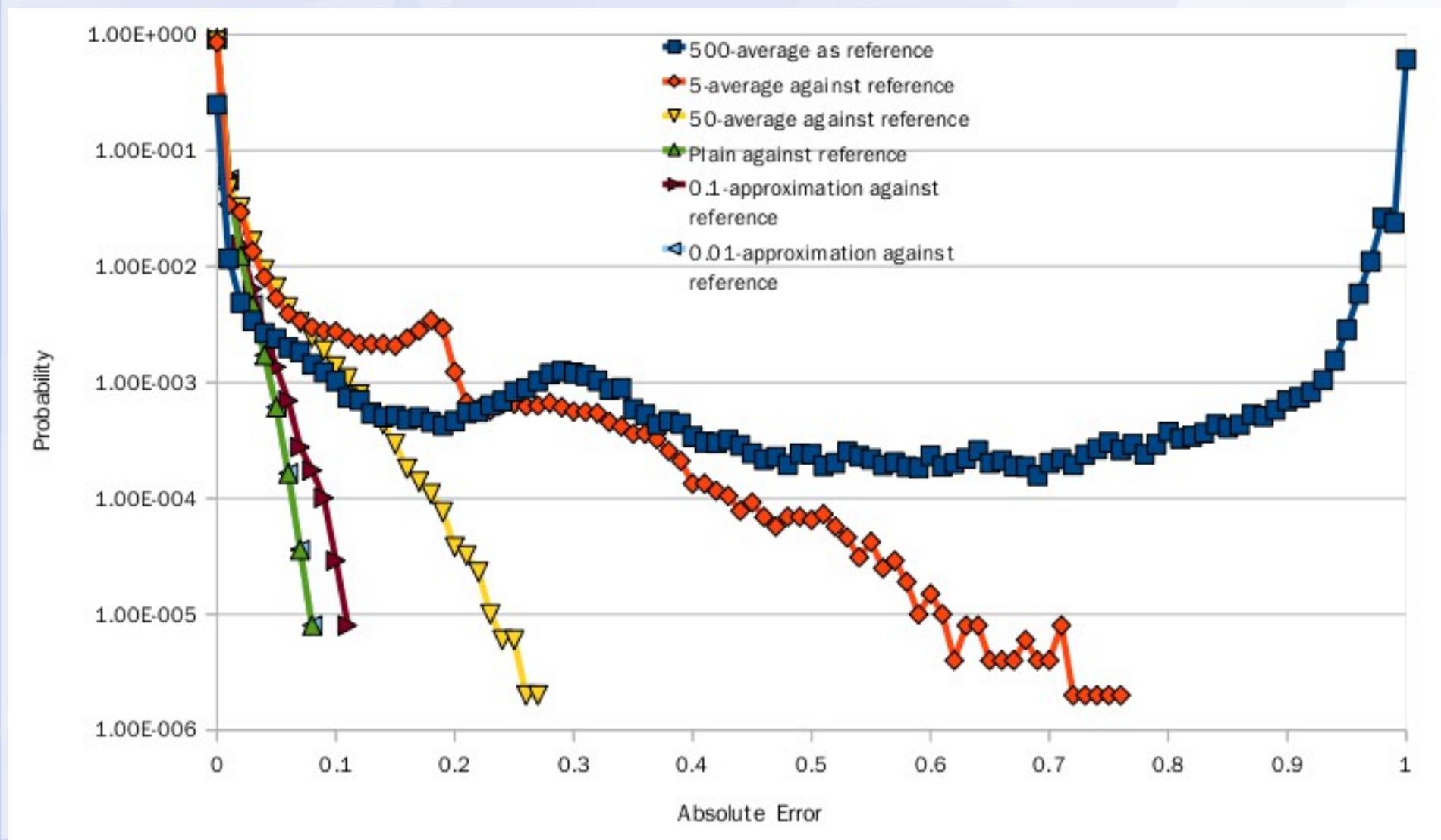
M=4, CPU2000/SWIM



M=8, LINPACK



M=64, LINPACK



Q&A

Thank you!